



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Takashi NISHIKAWA et al.

Serial No. 10/019,540

Filed: January 3, 2002

For: LAYERED STRUCTURE,

METHOD FOR

MANUFACTURING THE SAME,

AND SEMICONDUCTOR

ELEMENT

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) Attention: Applications Branch

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with
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Apple Computer

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8.3.02

REQUEST FOR CORRECTED FILING RECEIPT

Honorable Commissioner of Patents

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Sir:

The Filing Receipt received in the above-identified application has errors thereon due to
Patent Office error as shown in red on the attached copy.

In view of the above, it is requested that a Corrected Filing Receipt be issued.

Respectfully submitted,

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/019,540	01/03/2002	2811	1650	740819-724	12	23	4

CONFIRMATION NO. 7353

FILING RECEIPT



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APR 1 2002

Date Mailed: 04/01/2002

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Applicant(s)

Takashi Nishikawa, Osaka, JAPAN;

Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/JP00/04477 07/06/2000

Foreign Applications

JAPAN 11/192659 07/07/1999

Projected Publication Date: Not Applicable, filed prior to November 29,2000

Non-Publication Request: No

Early Publication Request: No

Title

~~Multilayered body, method for fabricating multilayered body, and semiconductor device~~Layered Structure, Method for Manufacturing the same, and Semiconductor
Preliminary Class Element

257

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4/8/02 By PHL
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